


**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Assistant Commissioner of Patents  
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Harold C. Moore

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February 18, 2003

Date of Signature

Re:           Application of:       Newell E. Chiesl  
              Serial No.:       09/960,441  
              Filed:           September 21, 2001  
              For:             Arrangement for Measuring Pressure on a  
                                  Semiconductor Wafer and an Associated  
                                  Method for Fabricating a Semiconductor  
                                  Wafer  
              Group Art Unit:   2812  
              Examiner:       Viktor Simkovic  
              Our Docket No.:   1003-0610

**TRANSMITTAL OF RESPONSE TO OFFICE ACTION**

Please find for filing in connection with the above patent application the  
following:

1.     Response to Office Action.

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2/19/03 lam

\* The fee has been calculated as shown below.

CLAIMS AS AMENDED				
	Claims Remaining After Amendment	Highest No. Paid For	Fee Calculation	Addit Fee
Total Claims	11	20	0 X 9	\$ 0.00
Independent Claims	2	3	0 X 42	\$ 0.00
Total Additional Fee Required				\$ 0.00

Please charge any fee deficiency, or credit any overpayment, to Deposit Account No. 13-0014; but not to include any payment of issue fees.

Respectfully Submitted,

MAGINOT, MOORE & BOWMAN



February 18, 2003

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Enclosures


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